



## **Faraday Technology Vietnam**

### *Synergy for Excellence*

#### **Who are WE:**

**Faraday Technology Corporation** is a leading fabless ASIC design and silicon IP (intellectual property) provider established in 1993 as the first Asian ASIC vendor. Faraday is listed on the Taiwan Stock Exchange (TWSE) under ticker number 3035.

**Faraday Technology Vietnam (FTV)** is established in Ho Chi Minh in 2019. We aim to be a full-fledged R&D center, spanning from RTL design to post-silicon verification.

- More than 30 years of professional IC design experience.
- Job functions include RTL design, RTL verification, Design-for-test (DFT), Static timing analysis, Physical design, Memory and Standard cell IP design & Layout etc.
- More than 50 tape-outs per year, with applications covering Consumer, Networking, IoT and Niche (Medical, Automotive, Aerospace) etc.
- 1st Tier Customers in major geographic location
- Open Sharing and Learning environment
- Accelerated IC design Career Path with Competitive Package

**Location:** Floor 16, Royal Tower B, Royal Center Building, 235 Nguyen Van Cu Street, Nguyen Cu Trinh Ward, District 1, Ho Chi Minh City, Vietnam

Faraday Technology Vietnam is thrilled to announce opening for **Internship** in the following positions:

#### **We are hiring:**

Physical Design Engineer  
DFT – STA Engineer  
Design Verification Engineer  
RTL Design  
Memory Circuit Design

**Requirement:**

Major: Electronics & Electricals, Electronics & Telecommunication, Computer Engineering, Computer Science, Physics or Related Majors.

Third or Final year students.

GPA at least 7.0/10 or 3.0/4.0

We are looking for talented students who interested in IC Design and Semiconductor Major.

Scan QR Code below to submit your CV + transcript:



**FOLLOW US:**

- ✉Email: [globalhr@faraday-tech.com](mailto:globalhr@faraday-tech.com)
- ➔LinkedIn: <https://lnkd.in/gMfeqA9D>
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- 🌐Website: [https://lnkd.in/gaVwe\\_NE](https://lnkd.in/gaVwe_NE)

Join us and become a world-class IC Designer!



# FARADAY INTERNSHIP PROGRAM

## BATCH SEPTEMBER 2024

### INTERNSHIP PROGRAM:

- 1/ 288 hours internship program.
- 2/ Learn to use the latest EDA tools for chip design.
- 3/ Demonstrate using "real - world" block design.
- 4/ Weekly review with Mentor/Manager.
- 5/ Immediate Job offer upon successful internship completion.



### WE ARE HIRING:

- 1/ **Physical Design Engineer**
- 2/ **DFT - STA Design Engineer**
- 3/ **Design Verification Engineer**
- 4/ **RTL Design Engineer**
- 5/ **Memory Circuit Design Engineer**

### REQUIREMENT:

- 1/ **Major:** Electronics & Electricals, Electronics & Telecommunication, Computer Engineering, Computer Science, Physics or Related Majors.
- 2/ **Third or Final year students.**
- 3/ **GPA** at least 7.0/10 or 3.0/4.0

SCAN QR CODE TO APPLY

### CONTACT US

[globalhr@faraday-tech.com](mailto:globalhr@faraday-tech.com)

[Faraday-Vietnam](#)

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APPLY NOW

